playing,

    wherein, when the reception state of the digital audio program data is satisfactory, the switching circuit extracts the digital audio program data output from the decoder circuit and when the reception state of the program digital audio data deteriorates, the switching circuit is controlled by a detection output of the detecting circuit to extract the commercial radio digital audio data stored in the memory circuit.

REMARKS

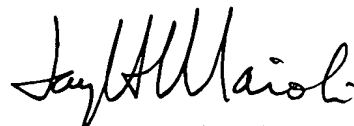   Claims 1-7 remain in the application and have been amended hereby.

    As will be noted from the Declaration, Applicants are citizens and residents of Japan and this application originated there.

    Accordingly, the amendments made to the specification are provided to place the application in idiomatic English, and the claims are amended to place them in better condition for examination.

    An early and favorable examination on the merits is earnestly solicited.

Respectfully submitted,
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VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

Please amend claims 1-7 by rewriting same to read as follows.

--1. (Amended) A digital audio broadcasting receiver comprising:

a receiving circuit for receiving multiplexed signals [for] representing a plurality of [programs] program data;

a separator circuit for separating the plurality of [multiplexed] program data from the multiplexed signals received by the receiving circuit;

a program data storage circuit for storing predetermined program data from among the plurality of program data separated by the separator circuit;

a switching circuit for switching between the stored program data [to be] output from the program data storage circuit and the other program data [which] that is not stored in the program storage circuit;

a decoder circuit for decoding [the] program data output by the switching circuit;

a D/A converter circuit for converting [the] decoded program data output from the decoder circuit into analog audio signals;

amplifying/playing means for amplifying and playing the analog audio signals output from the D/A converter circuit; and

a control circuit for controlling the [individual circuits] receiving circuit, the program data storage circuit, the switching circuit, the decoder circuit, the D/A converter circuit, and the amplifying/playing means.

--2. (Amended) [A] The digital audio broadcasting receiver according to Claim 1, wherein, when the digital audio

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broadcasting receiver stops receiving signals, the stored program data is selected, and [sound is] the analog audio signal are played.

--3. (Amended) [A] The digital audio broadcasting receiver according to Claim 1, wherein[,] at a predetermined time[,] the stored program data is selected, and [sound is] the analog audio signals are played.

--4. (Amended) A digital audio broadcasting receiver according to Claim 1, wherein[,] when the digital audio broadcasting receiver is turned ON, the stored program data is selected[,] and [sound is] the analog audio signals are played.

--5. (Amended) A broadcasting method for digital audio broadcasting, comprising the step of multiplexing at least one main program and at least one sub program,
wherein the sub program comprises an information service program including one of a commercial message [or] and news.

--6. (Amended) A broadcasting method for digital audio broadcasting [for multiplexing] of multiplexed digital audio data for a plurality of channels, comprising the step of broadcasting commercial digital audio data repetitively using at least one channel from among the plurality of channels.

--7. (Amended) A digital audio broadcasting receiver for receiving digital audio broadcasting implemented by multiplexing [program] digital audio program data and commercial radio digital audio data, comprising:
a decoder circuit for decoding and outputting the [program]

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digital audio <u>program</u> data and the commercial <u>radio</u> digital audio data;

a memory circuit for storing the commercial <u>radio</u> digital audio data output from the decoder circuit;

a switching circuit for selectively extracting the [program] digital audio <u>program</u> data output from the decoder circuit and the commercial <u>radio</u> digital audio data stored in the memory circuit; and

a detecting circuit for detecting a state in which a reception state of the [program] digital audio <u>program</u> data deteriorates and the program digital audio data becomes unsuitable for playing[;]⸴

wherein, when the reception state of the [program] digital audio <u>program</u> data is satisfactory, the switching circuit extracts the [program] digital audio <u>program</u> data output from the decoder circuit[;] and

when the reception state of the program digital audio data deteriorates, the switching circuit is controlled by [the] <u>a</u> detection output of the detecting circuit to extract the commercial <u>radio</u> digital audio data stored in the memory circuit.